Docket No. H302.278.101
System on a Chip Having a Non-Volatile Imperfect
M mory
Andrew M. Spencer et al.
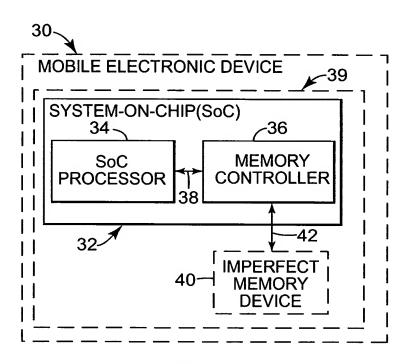1/2

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MOBILE ELECTRONIC DEVICE

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SYSTEM-ON-CHIP(SoC)

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36

| SoC PROCESSOR | MEMORY CONTROLLER |

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IMPERFECT MEMORY DEVICE

Fig. 1

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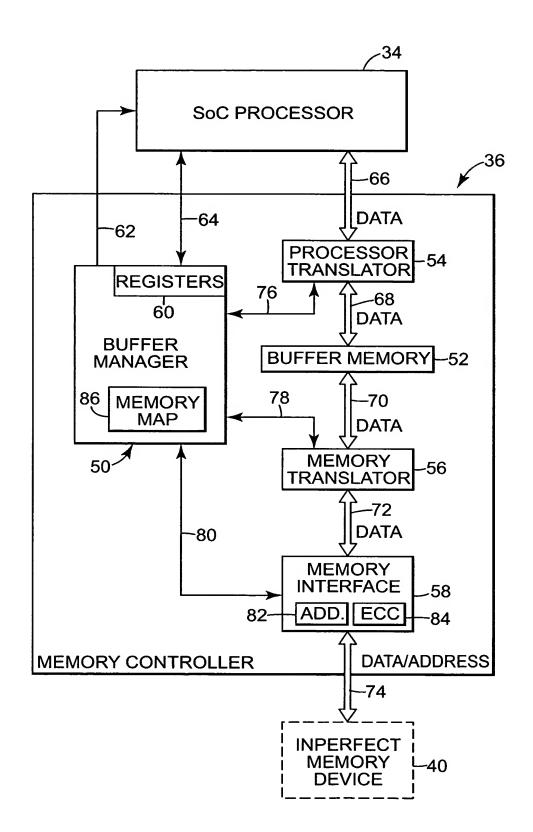**Memory**
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Fig. 2